

**Amendments to the Specification:**

Please amend the section entitled CROSS REFERENCE TO RELATED APPLICATIONS beginning on page 1 and extending to page 4 of the specification as follows:

The present invention is related to the following applications entitled “Method and Apparatus for Counting Instruction Execution and Data Accesses”, serial no. [[\_\_\_\_\_,]] 10/675,777, attorney docket no. AUS920030477US1, filed on September 30, 2003; “Method and Apparatus for Selectively Counting Instructions and Data Accesses”, serial no. [[\_\_\_\_\_,]] 10/674,604, attorney docket no. AUS920030478US1, filed on September 30, 2003; “Method and Apparatus for Generating Interrupts Upon Execution of Marked Instructions and Upon Access to Marked Memory Locations”, serial no. [[\_\_\_\_\_,]] 10/675,831, attorney docket no. AUS920030479US1, filed on September 30, 2003; “Method and Apparatus for Counting Data Accesses and Instruction Executions that Exceed a Threshold”, serial no. [[\_\_\_\_\_,]] 10/675,778, attorney docket no. AUS920030480US1, filed on September 30, 2003; “Method and Apparatus for Counting Execution of Specific Instructions and Accesses to Specific Data Locations”, serial no. [[\_\_\_\_\_,]] 10/675,776, attorney docket no. AUS920030481US1, filed on September 30, 2003; “Method and Apparatus for Debug Support for Individual Instructions and Memory Locations”, serial no. [[\_\_\_\_\_,]] 10/675,751, attorney docket no. AUS920030482US1, filed on September 30, 2003; “Method and Apparatus to Autonomically Select Instructions for Selective Counting”, serial no. [[\_\_\_\_\_,]] 10/675,721, attorney docket no. AUS920030483US1, filed on September 30, 2003; “Method and Apparatus to Autonomically Count Instruction Execution for Applications”, serial no. [[\_\_\_\_\_,]] 10/674,642, attorney docket no. AUS920030484US1, filed on September 30, 2003; “Method and Apparatus to Autonomically Take an Exception on Specified Instructions”, serial no. [[\_\_\_\_\_,]] 10/674,606, attorney docket no. AUS920030485US1, filed on September 30, 2003; “Method and Apparatus to Autonomically Profile Applications”, serial no. [[\_\_\_\_\_,]] 10/675,783, attorney docket no. AUS920030486US1, filed on September 30, 2003; “Method and Apparatus for Counting Instruction and Memory Location Ranges”, serial no. [[\_\_\_\_\_,]] 10/675,872, attorney docket no. AUS920030487US1, filed on September 30, 2003; “Method and Apparatus for Qualifying Collection of Performance Monitoring Events by Types of Interrupt When Interrupt Occurs”, serial no. [[\_\_\_\_\_,]] 10/757,186, attorney docket no. AUS920030540US1, filed on [[\_\_\_\_\_,]] January 14, 2004; “Method and Apparatus for Counting Interrupts by Type”, serial no. [[\_\_\_\_\_,]] 10/757,198, attorney docket no. AUS920030541US1, filed on [[\_\_\_\_\_,]] January 14, 2004; “Method and Apparatus for Providing Pre and Post Handlers for Recording Events”, serial no. [[\_\_\_\_\_,]] 10/757,192, attorney docket no. AUS920030543US1, filed on [[\_\_\_\_\_,]] January 14, 2004; “Autonomic Method and Apparatus for Counting Branch

Instructions to Improve Branch Predictions”, serial no. [[\_\_\_\_\_,]] 10/757,237, attorney docket no. AUS920030550US1, filed on [[\_\_\_\_\_,]] January 14, 2004; “Autonomic Method and Apparatus for Hardware Assist for Patching Code”, serial no. [[\_\_\_\_\_,]] 10/757,171, attorney docket no. AUS920030551US1, filed on [[\_\_\_\_\_,]] January 14, 2004; “Autonomic Method and Apparatus for Local Program Code Reorganization Using Branch Count Per Instruction Hardware”, serial no. [[\_\_\_\_\_,]] 10/747,156 attorney docket no. AUS920030552US1, filed on [[\_\_\_\_\_,]] January 14, 2004; “Method and Apparatus for Maintaining Performance Monitoring Structures in a Page Table for Use in Monitoring Performance of a Computer Program”, serial no. [[\_\_\_\_\_,]] 10/747,250, attorney docket no. AUS920030488US1, filed on [[\_\_\_\_\_,]] January 14, 2004; “Method and Apparatus for Counting Instruction Execution and Data Accesses to Identify Hot Spots”, serial no. [[\_\_\_\_\_,]] 10/757,248, attorney docket no. AUS920030544US1, filed on [[\_\_\_\_\_,]] January 14, 2004; “Method and Apparatus for Autonomic Detection of Cache 'Chase Tail' Conditions and Storage of Instructions/Data in 'Chase Tail' Data Structure”, serial no. [[\_\_\_\_\_,]] 10/757,256, attorney docket no. AUS920030545US1, filed on [[\_\_\_\_\_,]] January 14, 2004; “Method and Apparatus for Autonomically Initiating Measurement of Secondary Metrics Based on Hardware Counter Values for Primary Metrics”, serial no. [[\_\_\_\_\_,]] 10/757,269, attorney docket no. AUS920030549US1, filed on [[\_\_\_\_\_,]] January 14, 2004; “Method and Apparatus for Generating Interrupts Based on Arithmetic Combinations of Performance Counter Values”, serial no. [[\_\_\_\_\_,]] 10/757,212, attorney docket no. AUS920030553US1, filed on [[\_\_\_\_\_,]] January 14, 2004; “Method and Apparatus for Identifying False Cache Line Sharing”, serial no. [[\_\_\_\_\_,]] 10/757,249, attorney docket no. AUS920030554US1, filed on [[\_\_\_\_\_,]] January 14, 2004; and “Method and Apparatus for Autonomically Moving Cache Entries to Dedicated Storage When False Cache Line Sharing is Detected”, serial no. [[\_\_\_\_\_,]] 10/757,227, attorney docket no. AUS920030555US1, filed on [[\_\_\_\_\_,]] January 14, 2004. All of the above related applications are assigned to the same assignee, and incorporated herein by reference.